

AMENDMENT TO THE SPECIFICATION

Please amend the paragraph beginning on page 6, line 13 as follows:

--In the inventive semiconductor device, the timing adjustment circuit block preferably includes: a [[pulse]] determination period signal generating circuit for generating and outputting a [[pulse]] determination period signal for determining the propagation timing of the transmission signal, based on a clock signal for determining the propagation timing of the transmission signal; a delay element block which includes at least one delay element and in which a delay is added to the transmission signal; and a fuse circuit which includes at least one fuse, the fuse being melted down based on the [[pulse]] determination period signal and a transmission signal which has passed through the delay element block.--